

FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 30585/9		SERIAL NO. 09/425,401	
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				APPLICANT John S. Yates, Jr., et al.			
				FILING DATE October 22, 1999		GROUP ART UNIT 2751	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		3,544,969	12/01/70	Rakoczi	340	172.5	Nov. 27, 1967
		3,781,823	12/25/73	Senese	340	172.5	July 28, 1972
		4,084,235	04/11/78	Hirtle	364	200	Apr. 14, 1975
		4,455,602	06/19/84	Baxter	364	200	May 22, 1981
		4,575,797	03/11/86	Gruner	364	200	May 22, 1981
		5,638,525	06/10/97	Hammond	395	385	Feb. 10, 1995
		5,796,939	8/18/98	Berc, et al.	395	184.01	Mar. 10, 1997
		5,802,272	9/1/98	Sites, et al.	395	183.21	Dec. 19, 1994
		5,822,578	10/13/98	Frank	395	591	June 5, 1995
		5,832,205	11/03/98	Kelly	395	185.06	Aug. 20, 1996
		5,930,509	07/27/99	Yates	395	707	Jan. 29, 1996
		5,958,047	09/28/99	Panwar	712	237	Jun. 25, 1997
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
		WO 96/24895	08/15/96	PCT/US	G06F	9/22	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)							
		Burroughs, Preliminary Edition, B 1700 Systems Reference Manual, Burroughs Corporation (1972)					
		Burroughs, B 1700 Systems Micro Implementation Language (MIL) Reference Manual, Burroughs Corporation (1973)					
		Anton Chernoff, et al., FX132, A Profile-Directed Binary Translator, IEEE Micro, Vol. 18 No. 2 pp. 56-64 (March/April, 1998)					
		T.M. Conte, et al., "Accurate and practical profile-driven compilation using the profile buffer," Proceedings of the 29 th Annual International Symposium on Microarchitecture, Paris, France, pp. 36-45 (Dec. 1996)					
		T.M. Conte, et al., "Hardware based profiling: An effective technique for profile-driven optimization," International Journal of Parallel Programming, vol. 24, no. 2 (Feb. 1996)					
		T.M. Conte, et al., "Using branch handling hardware to support profile-driven optimization," Proceedings of the 27 th Annual International Symposium on Microarchitecture San Jose, CA (Dec. 1994)					
EXAMINER <i>Chan, G</i>				DATE CONSIDERED <i>4-15-04</i>			

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EORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 30585/9	SERIAL NO. 09/425,401
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)					
			J.S. Cox et al., "Commercializing profile-driven optimization." Proceedings of the 28 th Hawaii International Conference on System Sciences, vol. 1 Maui, HI, pp. 221-228 (Jan. 1995)		
			Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) http://www.digital.com/semiconductor/amt/fx32/fx-white.htm		
			Ebcioglu and Altman, IBM Research Report, DAISY: Dynamic Compilation for 100% Architectural Compatibility, IBM Research Division (1996)		
			Ronald M. Guffin, Microdiagnostics for the Standard Computer MLP-900 Processor, IEEE Transactions on Computers, Vol. C-20 No. 7, pp. 803-808 (July 1971)		
			Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64 Technology and Market Forecast, IA-64 Software Model - Chapter 3 excerpt Microprocessor Report, MicroDesign Resources (July 20, 1998)		
			Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64: Technology and Market Forecast, Executive Summary, Microprocessor Report, MicroDesign Resources (July 20, 1998)		
			Linley Gwennap, First Merced Patent Surfaces, Intel Document Reveals Processors With Dual Instruction Sets, Microprocessor Report, MicroDesign Resources (July 20, 1998)		
			R.E. Hank, et al., Proceeding of the 28 th Annual International Symposium on Microarchitecture, pp. 158-168 (Dec. 1995)		
			Raymond J. Hookway, et al., Combining Emulation and Binary Translation (August 28, 1997) http://www.digital.com/info/DTJP01HM.HTM		
			Harold W. Lawson, Jr., et al., Functional Characteristics of a Multilingual Processor, IEEE Transactions on Computers, Vol. C-20, No. 7, pp. 732-742 (July, 1971)		
			K.N.P. Menezes, "Hardware-based profiling for program optimization," Ph.D. thesis, Department of Electrical and Computer Engineering, North Carolina State University, Raleigh, North Carolina (1997)		
			Nanodata Corporation, QM-1 Hardware level User's Manual (1975)		
			Nanodata Corporation, QM-2 Processor (1976)		
			Robert F. Rosin, Contemporary Concepts of Microprogramming and Emulation, Computing Surveys, Vol. 1 No. 4, pp. 197-212 (December 1969)		
			Robert F. Rosin, An Environment for Research in Microprogramming and Emulation, Communications of the ACM, Vol. 15 No. 8, pp. 748-760 (August 1972)		
			Standard Computer Corp., IC-9000 Central Processing Unit - Principles of Operation (undated - possibly 1969)		
			W.T. Wilner, Design of the Burroughs B1700, Fall Joint Computer Conference, pp. 489-497 (1972)		
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U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
5231.9-4016SERIAL NO.
09/425,401

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INFORMATION DISCLOSURE CITATION

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APPLICANT

John S. Yates, Jr., et al.

FILING DATE

October 22, 1999

GROUP ART UNIT

251 2/24 FEB 25 2002

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Technology Center 2100

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Not Considered	4,575,797	03/11/1986	Gruner	364	200	May 22, 1981
Not Considered	5,542,109	07/30/1996	Blomgren	395	800	Aug. 31, 1994
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Not Considered	5,832,205	11/03/1998	Kelly	395	185.06	Aug. 20, 1996
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					YES	NO
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)

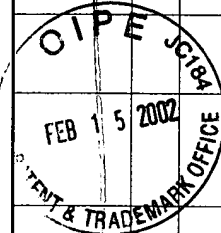
	Burroughs, Preliminary Edition, B 1700 Systems Reference Manual, Burroughs Corporation (1972)
	Burroughs, B 1700 Systems Micro Implementation Language (MIL) Reference Manual, Burroughs Corporation (1973)
Not Considered	Anton Chernoff, et al., FX!32, A Profile-Directed Binary Translator, IEEE Micro, Vol. 18 No. 2 pp. 56-64 (March/April, 1998)

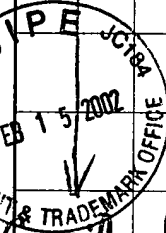
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DATE CONSIDERED

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INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				APPLICANT John S. Yates, Jr., et al.	
				FILING DATE October 22, 1999	GROUP ART UNIT 2751 2124
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)					
<i>Not Considered</i> 		Compaq Computer Corp., Compiler Writer's Guide for the Alpha 21264 (1999) Technology Center 2100			
		T.M. Conte, et al., "Accurate and practical profile-driven compilation using the profile buffer," Proceedings of the 29 th Annual International Symposium on Microarchitecture, Paris, France, pp. 36-45 (Dec. 1996)			
		T.M. Conte, et al., "Hardware based profiling: An effective technique for profile-driven optimization," International Journal of Parallel Programming, vol. 24, no. 2 (Feb. 1996)			
		T.M. Conte, et al., "Using branch handling hardware to support profile-driven optimization," Proceedings of the 27 th Annual International Symposium on Microarchitecture San Jose, CA (Dec. 1994)			
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		Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) http://www.digital.com/semiconductor/amt/fx32/fx-white.htm			
		Ebcioglu and Altman, IBM Research Report, DAISY: Dynamic Compilation for 100% Architectural Compatibility, IBM Research Division (1996)			
		Linda Geppert et al., Transmeta's Magic Show, IEEE Spectrum, vol. 37 no. 5, pp. 26-33 (May 2000)			
		Ronald M. Guffin, Microdiagnostics for the Standard Computer MLP-900 Processor, IEEE Transactions on Computers, Vol. C-20 No. 7, pp. 803-808 (July 1971)			
		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64 Technology and Market Forecast, IA-64 Software Model - Chapter 3 excerpt Microprocessor Report, MicroDesign Resources (July 20, 1998)			
		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64: Technology and Market Forecast, Executive Summary, Microprocessor Report, MicroDesign Resources (July 20, 1998)			
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		R.E. Hank, et al., Proceeding of the 28 th Annual International Symposium on Microarchitecture, pp. 158-168 (Dec. 1995)			
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K.N.P. Menezes, "Hardware-based profiling for program optimization," Ph.D. thesis, Department of Electrical and Computer Engineering, North Carolina State University, Raleigh, North Carolina (1997)					
EXAMINER <i>Chao, J</i>		DATE CONSIDERED <i>4-14-04</i>			

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INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				APPLICANT John S. Yates, Jr., et al.	
				FILING DATE October 22, 1999	GROUP ART UNIT 2751 2124
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)					
<i>Not Considered</i> 		Nanodata Corporation, QM-1 Hardware level User's Manual (1975)			
		Nanodata Corporation, QM-2 Processor (1976)			
		Robert F. Rosin, Contemporary Concepts of Microprogramming and Emulation, Computing Surveys, Vol. 1 No. 4, pp. 197-212 (December 1969)			
		Robert F. Rosin, An Environment for Research in Microprogramming and Emulation, Communications of the ACM, Vol. 15 No. 8, pp. 748-760 (August 1972)			
		Standard Computer Corp., IC-9000 Central Processing Unit - Principles of Operation (undated - possibly 1969)			
		W.T. Wilner, Design of the Burroughs B1700, Fall Joint Computer Conference, pp. 489-497 (1972)			
<i>Not Considered</i>		W.T. Wilner, Burroughs B1700 Memory Utilization, Fall Joint Computer Conference, pp. 579-586 (1972)			
EXAMINER <i>Chas. G.</i>			DATE CONSIDERED <i>4-14-04</i>		

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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

APPLICANT

John S. Yates, et al.

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October 22, 1999

GROUP ART UNIT

2151

2124

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Not Considered	4,514,803	04/30/1985	Agnew	364	200	Apr. 26 1982
11	4,779,187	10/18/1988	Letwin			Apr. 10, 1985
	4,812,975	03/14/1989	Adachi	364	300	Jul. 11, 1984
	5,115,500	05/19/1992	Larsen			Jan. 11, 1988
	5,155,835	10/13/1992	Belsan			Nov. 19, 1990
	5,241,664	08/31/1993	Ohba			Jan. 7, 1991
	5,301,287	04/05/1994	Herrell			Feb. 16, 1993
	5,307,504	04/26/1994	Robinson	395	800	Mar. 7, 1991
	5,335,331	08/02/1994	Murao			Jul. 12, 1991
	5,361,340	11/01/1994	Kelly			Mar. 9, 1993
	5,432,795	07/11/1995	Robinson	371	19	May 15, 1991
	5,454,117	09/26/1995	Puziol	395	800	Aug. 25, 1993
	5,481,684	01/02/1996	Richter	395	375	Jul. 20 1994
	5,481,693	01/02/1996	Blomgren	395	375	Jul. 20, 1994
	5,515,518	06/07/1996	Stiles	395	375	Jul. 5, 1994
	5,542,109	07/30/1996	Blomgren	395	800	Aug. 31, 1994
	5,568,646	10/22/1996	Jaggar			Sep. 19, 1994
	5,574,927	11/12/1996	Scantlin	395	800	Nov. 12, 1996
	5,598,546	01/28/1997	Blomgren	395	385	Aug. 31, 1994
	5,608,886	03/04/1997	Blomgren	395	586	Aug. 31, 1994
	5,619,666	04/08/1997	Coon et al.			Jun. 2, 1995
	5,628,016	05/06/1997	Kukol	395	705	June 15, 1994
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	5,751,982	05/12/1998	Morley	395	800	Mar. 31, 1995
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Chen, J

DATE CONSIDERED

4-15-03

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John S. Yates, et al.

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GROUP ART UNIT

245T 2/24

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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	5,781,792	07/14/1998	Asghar	395	800.35	Mar. 18, 1996
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	5,884,057	03/16/1999	Blomgren	395	375	May 8, 1995
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DATE CONSIDERED

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APPLICANT

John S. Yates, et al.

FILING DATE

October 22, 1999

GROUP ART UNIT

2151

2124

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,081,890	06/27/2000	Datta	713	1	Nov. 30, 1998
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Chen, G

DATE CONSIDERED

4-15-04

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FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE CITATION

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APPLICANT

John S. Yates, et al.

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GROUP ART UNIT

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
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